

Assignee: Intel Corporation

REMARKS

Applicant respectfully requests reconsideration of this application as amended. New claims 20 through 38 have been added. Claims 20 through 38 are pending in this application. Claims 1 through 19 are cancelled without prejudice to their subsequent prosecution.

SPECIFICATION ISSUES

In the Office Action, at paragraph 3, the specification was objected to at two locations. Applicants have amended the specification in those two locations to overcome this objection.

CLAIM OBJECTION ISSUES

In the Office Action, at paragraphs 4 through 9, several claims were objected to. Applicants have cancelled claims 1 through 19, and therefore submit that these objections are presently moot.

SECTION 102 ISSUES

In the Office Action, at paragraph 11, claims 1, 2. 6. 7, 11, and 12 stand rejected under 35 U.S.C. §102(b) as being anticipated by Hennessy and Patterson "Computer Architecture – A Quantitative Approach", 2nd Edition, 1996 (hereinafter *Hennessy*). Claims 1 through 19 have been cancelled without prejudice to their re-introduction at a subsequent

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stage of the prosecution of the present application. Applicants submit that this rejection is currently moot.

Applicants wish to point out that *Hennessy* does not address the allocating of register files to an instruction that may write to many predicate registers, and that therefore applicants submit that *Hennessy* does not anticipate the pending claims 20 through 38.

SECTION 103 ISSUES

In the Office Action, at paragraphs 19, 23, and 36, claims 1 through 19 stand rejected under 35 U.S.C. §103(a) as being anticipated by a combination of *Hennessy*, Nojiri, U.S. Patent 5,179,685, Parady, U.S. Patent 5,933,627, and Gschwind et al., U.S. Patent 6,513,109.

Claims 1 through 19 have been cancelled without prejudice to their re-introduction at a subsequent stage of the prosecution of the present application. Applicants submit that this rejection is currently moot.

Applicants wish to point out that none of the *Hennessy*, Nojiri, U.S. Patent 5,179,685, Parady, U.S. Patent 5,933,627, and Gschwind et al., U.S. Patent 6,513,109 references address the allocating of register files to an instruction that may write to many predicate registers, and that therefore applicants submit that these references do not anticipate the pending claims 20 through 38.

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SUMMARY

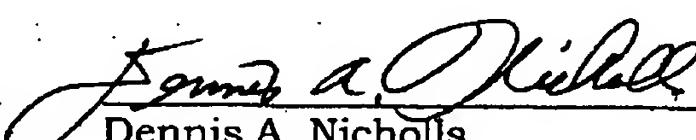
Applicants believe that all pending claims are allowable over the cited art of record. Applicants therefore respectfully request that all pending claims 20 through 38 be allowed.

If the Examiner finds any remaining impediment to the prompt allowance of these claims that could be clarified with a telephone conference, the Examiner is respectfully requested to contact applicants' representative, Dennis A. Nicholls, at (408) 765-5789.

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due.

Respectfully submitted,

Date: November 15, 2004


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